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Attorney Docket No. 1207.P001C5

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Benjamin S. Ting

Serial No. 10/692,880

Filed: October 23, 2003

For: ARCHITECTURE AND
INTERCONNECT SCHEME FOR
PROGRAMMABLE LOGIC
CIRCUITS

EXAMINER: LIN, SUN J.

ART UNIT: 2825

Commissioner for Patents
PO Box 1450
Alexandria, Virginia 22313-1450TERMINAL DISCLAIMER UNDER 37 C.F.R. § 1.321(c)

The undersigned attorney represents that the undersigned attorney is the attorney of record for the above-referenced patent application.

The assignee of the entire right, title, and interest in and to the above-referenced patent application is BTR, Inc. ("assignee"),
(Name of Assignee)

a Nevada corporation having a place of business at
(State of Incorporation)

1 East 1st Street
(Address)

Reno, Nevada 89501

10/14/2005 HDEESS1 00000081 022666 10692880
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Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

By: 

Dated: 10/13/2005

Name: James C. Scheller

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By 

Juanita Briscoe

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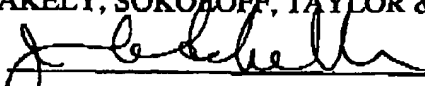
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